

















Datasheet

Ortustech

COM35H3R12ULC

OR-20-052

The information contained in this document has been carefully researched and is, to the best of our knowledge, accurate. However, we assume no liability for any product failures or damages, immediate or consequential, resulting from the use of the information provided herein. Our products are not intended for use in systems in which failures of product could result in personal injury. All trademarks mentioned herein are property of their respective owners. All specifications are subject to change without notice.

S	PECIFICATIONS № 22TLM07	73	(1/33) Issue:Oct.5,20
		This product is under de specifications are sub	
	Spacificat	tions for	
	Specificat		
<u>Blanvie</u>	w TFT-LCD M	onitor (TENTAT	IVE)
	(3.5*" VGA 480 x RC	GB x 640 Portrait)	
(1	<u>Version</u> Please be sure to check the sp		
	MODEL COM3	5H3R12ULC	
Customer's Approval			
Signature :			
Signature :			
Name :			
Section :			
Title :			
Date :			
ODT		• • •	
ORI	USTEC	.н	
		TOPPAN INC. Electronics Division	
		Ortus Subdivision	
		Approved by	
		Checked by	
		Prepared by	
	TOPPAN	N INC.	

SPECIFICATIONS № 22TLM073

Version History

Ver.	Date	Page	Description				
0.0	Oct.5,2022	-	- Tentative issue				
			TOPPAN INC.				

(2/33)

Contents

1.	Application		4
	Outline Specifications		
	2.1 Features of the Product		5
	2.2 Display Method		5
3	Dimensions and Shape		•
•	3.1 Dimensions		7
	3.2 Outward Form		8
	3.3 Serial № print (S-print)		9
4	Pin Assignment		10
	Block Diagram		11
	Absolute Maximum Rating		12
	Recommended Operating Conditions		12
	Electrical Characteristics		
•	8.1 DC Characteristics		13
	8.2 AC Characteristics		14
	8.3 Input Timing Characteristics		16
	8.4 Driving Timing Chart		17
	8.5 Example of Driving Timing Chart		18
9.	Power ON/OFF sequence		19
	Characteristics		
	10.1 Optical Characteristics	• • • • • • • • • •	20
	10.2 Temperature Characteristics	• • • • • • • • • •	21
11.	Criteria of Judgment		
	11.1 Defective Display and Screen Quality	• • • • • • • • • •	22
	11.2 Screen and Other Appearance	• • • • • • • • • •	23
12.	Reliability Test	• • • • • • • • • •	24
13.	Packing Specifications	• • • • • • • • • •	26
14.	Handling Instruction		
	14.1 Cautions for Handling LCD panels	••••	27
	14.2 Precautions for Handling	••••	28
	14.3 Precautions for Operation	• • • • • • • • • •	28
	14.4 Storage Condition for Shipping Cartons	• • • • • • • • • •	29
	14.5 Precautions for Peeling off		
	the Protective film	•••••	30
	14.6 Warranty	•••••	30
л	PPENDIX		31
A			51

1. Application

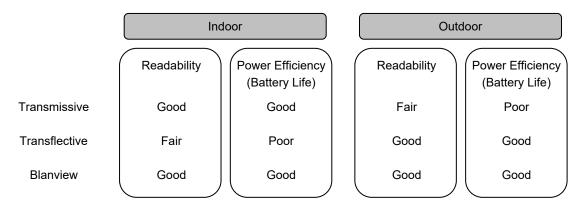
This Specification is applicable to 88.8 mm (3.5 inch) Blanview TFT-LCD monitor for non-military use.

- O TOPPAN makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and TOPPAN shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties. Since this Specification contains TOPPAN's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of TOPPAN's confidential information and copy right.
- If Purchaser intends to use this Products for an application which requires higher level of reliability and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.), disaster-prevention/security equipment or various safety equipment, Purchaser shall consult TOPPAN on such use in advance.
- This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.
- ◎ It must be noted as an mechanical design manner, especial attention in housing design to prevent arcuation/flexure caused by stress to the LCD module shall be considered.
- TOPPAN assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.
- It shall be mutually conferred if nonconforming defect which result from unspecified cause in this specification arises.
- ◎ If any issue arises as to information provided in this Specification or any other information, TOPPAN and Purchaser shall discuss them in good faith and seek solution.
- O TOPPAN assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.
- ◎ This Product is compatible for RoHS(2.0) directive.

Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series (PBB series)	1000
Polybrominated biphenyl ether series (PBDE series)	1000
Bis(2-ethylhexyl)phthalate series(DEHP series)	1000
Butyl benzyl phthalate series(BBP series)	1000
Dibutyl phthalate series(DBP series)	1000
Diisobutyl phthalate series(DIBP series)	1000

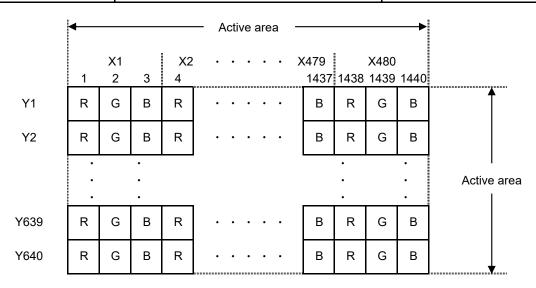
2. Outline Specifications

- 2.1 Features of the Product
 - 3.5 inch diagonal display, 1440 [H] x 640 [V] dots.
 - 6-bit / 262,144 colors.
 - Timing generator [TG], Counter-electrode driving circuitry, Built-in power supply circuit.
 - Power save (Standby) mode capable.
 - Long life & High bright white LED back-light.
 - Blanview TFT-LCD, improved outdoor readability.

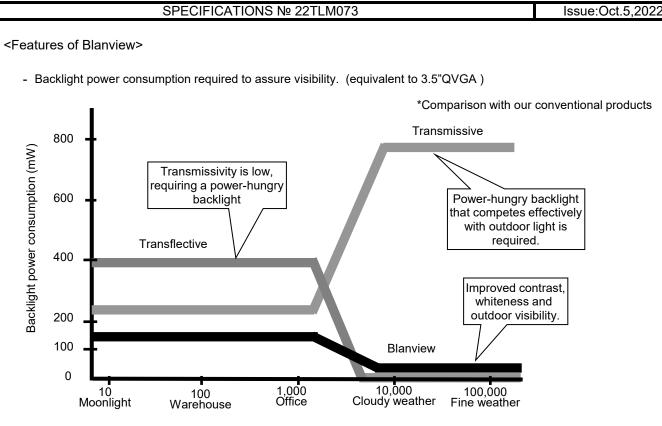


2.2 Display Method

Items	Specifications	Remarks
Display type	VA , 262,144 colors.	
	Blanview, Normally black.	
Driving method	a-Si TFT Active matrix.	
	Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to "Dot arrangement"
Signal input method	6-bit RGB,parallel input.	
Backlight type	Long life & High bright white LED.	
NTSC ratio	50%	



Dot arrangement (FPC cable placed leftside)

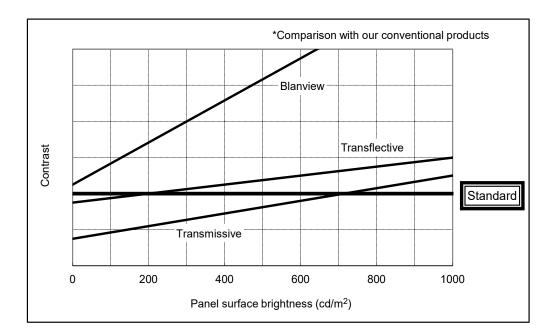


(6/33)

Surrounding illumination (Ix)

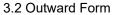
- Contrast characteristics under 100,000lx. (same condition as direct sunlight.) With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.

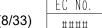
Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line. (TOPPAN criteria)



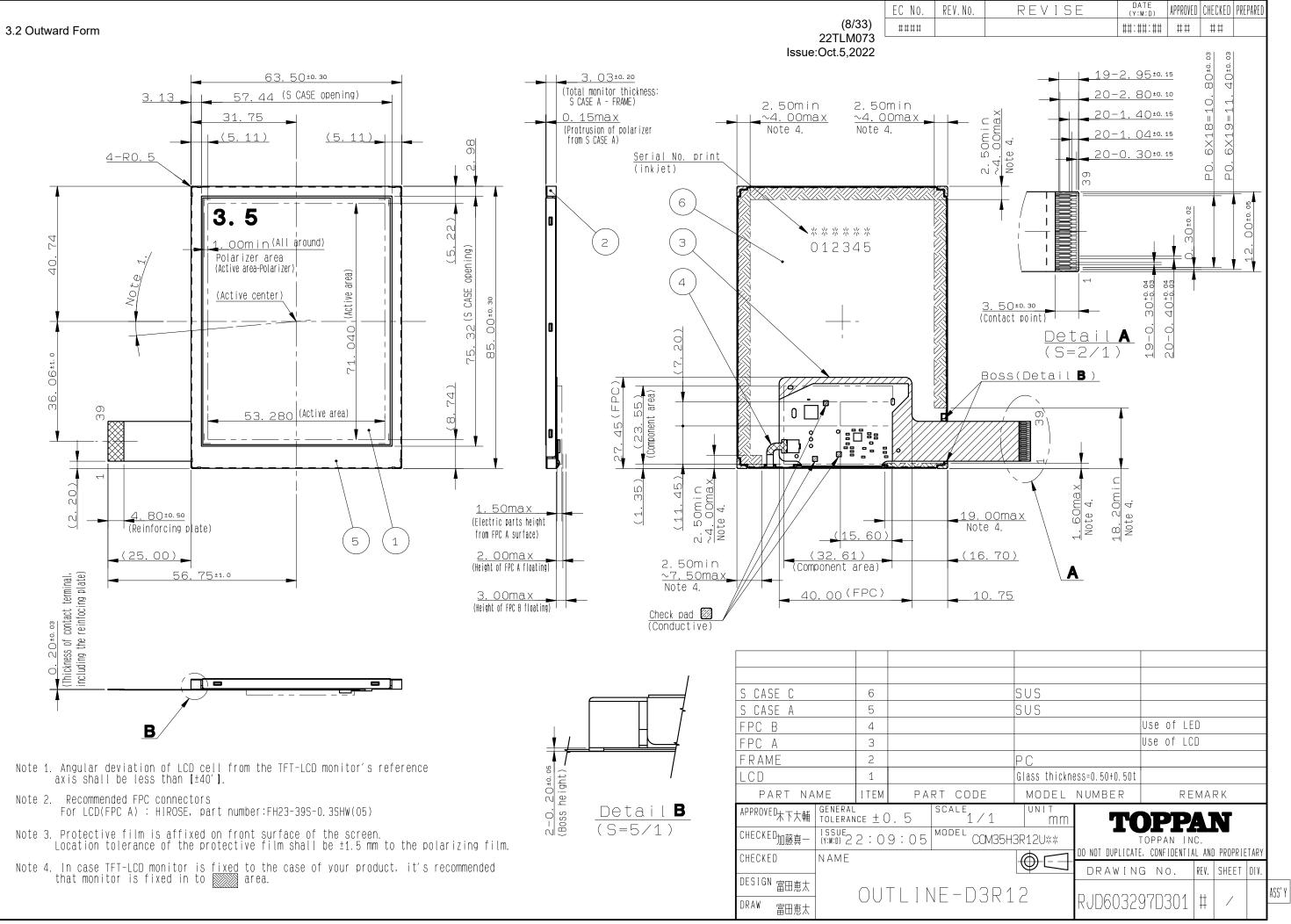
3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	63.50[H] × 85.00[V] ×3.03[D]	mm	Exclude FPC cable and
			parts on FPC.
Active area	53.28[H] × 71.04[V]	mm	88.8 mm diagonal
Number of dots	1440[H] × 640[V]	dot	
Dot pitch	37.00[H] × 111.00[V]	μm	
Surface hardness of the polarizer	3	Н	Load:2.0N
Weight	30.8	g	Include FPC cable





22TLM073



^{2021.7} Electronics Division

3.3 Serial Nº print (S-print)

3.3.1 Display Items

S-print indicates the least significant digit of manufacture year (1digit), manufacture month with below alphabet (1letter), model code (5characters), serial number (6digits).

* Contents of Display

<u>* * * *****</u> ****** a b c d

	Contents of display							
а	The least significant d	igit of manufacture ye	ar					
b	Manufacture month	Jan-A	May-E	Sep-I				
		Feb-B Jun-F Oct-J						
		Mar-C Jul-G Nov-K						
		Apr-D Aug-H Dec-L						
С	Model code 35RVC (Made in Japan)							
	35RWC (Made in Malaysia)							
d	Serial number							

* Example of indication of Serial № print (S-print)

Made in Japan

2L35RVC000125

means "manufactured in December 2022, 3.5" RV type, C specifications, serial number 000125"

·Made in Malaysia

2L35RWC000125

means "manufactured in December 2022, 3.5" RW type, C specifications, serial number 000125"

- 3.3.2 Location of Serial № print (S-print) Refer to 3.2 "Outward Form".
- 3.3.3 Others

Please note that it is likely to disappear with an organic solvent about the Serial print.

SPECIFICATIONS № 22TLM073

4. Pin Assignment

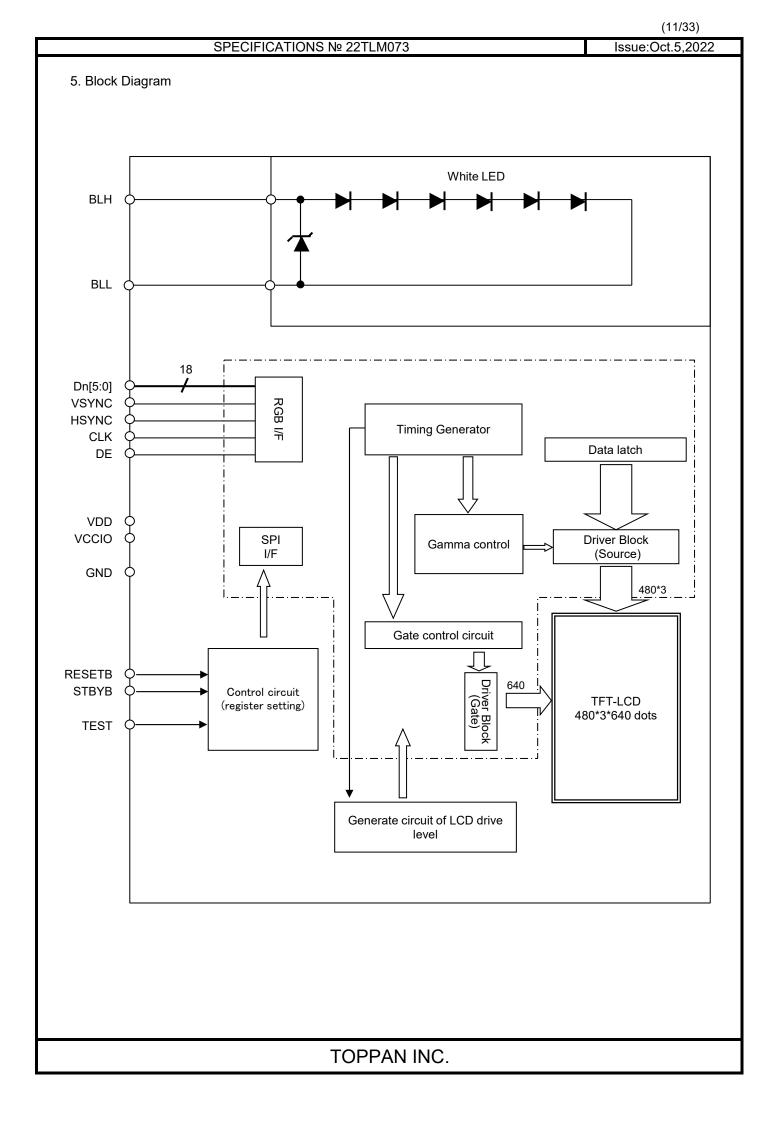
1 VSS Ground 2 VSS Ground 3 VDD Power supply input. 4 VCCIO Logic Interface Power supply input. 5 VSS Ground 6 RESETB System reset signal input.(Lo: active) 7 HSYNC Horizontal sync signal input.(Negative polarity) 8 VSYNC Vertical sync signal input.(Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 Oth for black display 13 D02 D00: LSB D05: MSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 D06: LSB D15: MSB 20 D13 D10: LSB D15: MSB 21 D14 Driver IC carries out gamma conversion internally. 22 D15 D122 D20: LSB 23 D22 D21 Oth for bl	No.	Symbol	Function
3 VDD Power supply input. 4 VCCIO Logic Interface Power supply input. 5 VSS Ground 6 RESETB System reset signal input.(Negative polarity) 7 HSYNC Horizontal sync signal input.(Negative polarity) 8 VSYNC Vertical sync signal input.(Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 Oth for black display 13 D02 D00: LSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 17 D10 Display data input for (G). 18 D11 Oth for black display 19 D12 D10: LSB 20 D13 Driver IC carries out gamma conversion internally. 21 D14 Driver IC carries out gamma conversion internally. 22 D15 D22 23 D20 Display data input for (R). 24 D21 <t< td=""><td>1</td><td>VSS</td><td>Ground</td></t<>	1	VSS	Ground
4 VCCIO Logic Interface Power supply input. 5 VSS Ground 6 RESETB System reset signal input.(Lo: active) 7 HSYNC Vertical sync signal input.(Negative polarity) 8 VSYNC Vertical sync signal input.(Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 Obh for black display 13 D02 D00:LSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 Display data input for (G). 17 D10 Display data input for (G). 18 D11 Obh for black display 19 D12 D10:LSB D15:MSB 20 D13 Driver IC carries out gamma conversion internally. 21 D14 Driver IC carries out gamma conversion internally. 22 D15 D22 D21 23 D20 Display data input for (R). D21:LSB 24 D21	2	VSS	Ground
5 VSS Ground 6 RESETB System reset signal input. (Lo: active) 7 HSYNC Horizontal sync signal input. (Negative polarity) 8 VSYNC Vertical sync signal input. (Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 Obh for black display 13 D02 D00:LSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 17 D10 Display data input for (G). 18 D11 O0h for black display 19 D12 D10:LSB 20 D13 21 D14 Driver IC carries out gamma conversion internally. 22 D15 23 D20 Display data input for (R). 24 D21 Oth for black display 25 D22 D21 26 D23 27 D24 28 D25 29 VSS 30 DE 31 STBYB 32 Ground 33 NC	3	VDD	Power supply input.
5 VSS Ground 6 RESETB System reset signal input. (Lo: active) 7 HSYNC Horizontal sync signal input. (Negative polarity) 8 VSYNC Vertical sync signal input. (Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 Obh for black display 13 D02 D00:LSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 17 D10 Display data input for (G). 18 D11 O0h for black display 19 D12 D10:LSB 20 D13 21 D14 Driver IC carries out gamma conversion internally. 22 D15 23 D20 Display data input for (R). 24 D21 Oth for black display 25 D22 D21 26 D23 27 D24 28 D25 29 VSS 30 DE 31 STBYB 32 Ground 33 NC	4	VCCIO	Logic Interface Power supply input.
7 HSYNC Horizontal sync signal input. (Negative polarity) 8 VSYNC Vertical sync signal input. (Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 00h for black display 13 D02 D00: LSB D05: MSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 00h for black display 17 D10 Display data input for (G). 18 D11 00h for black display 19 D12 D10: LSB D15: MSB 20 D13 D10: LSB D15: MSB 21 D14 Driver IC carries out gamma conversion internally. D10: LSB 23 D20 Display data input for (R). D04 to rblack display 25 D22 D21 00h for black display D20: LSB D25: MSB 26 D23 Driver IC carries out gamma conversion internally. D14 D14 D14 29 VSS <t< td=""><td>5</td><td>VSS</td><td></td></t<>	5	VSS	
8 VSYNC Vertical sync signal input.(Negative polarity) 9 CLK Clock input for display. (Data Input on the falling edge) 10 VSS Ground 11 D00 Display data input for (B). 12 D01 00h for black display 13 D02 D00:LSB D05:MSB 14 D03 Diver IC carries out gamma conversion internally. 16 D05 00h for black display 17 D10 Display data input for (G). 18 D11 00h for black display 19 D12 D10:LSB D15:MSB 20 D13 Diver IC carries out gamma conversion internally. 21 D14 Driver IC carries out gamma conversion internally. 22 D15 Display data input for (R). 23 D20 Display data input for (R). 24 D21 Oth for black display 25 D22 D20:LSB D25:MSB 26 D23 Diver IC carries out gamma conversion internally. 28 D25 Diver IC carries out gamma conversion internally. 29	6	RESETB	System reset signal input.(Lo: active)
9CLKClock input for display. (Data Input on the falling edge)10VSSGround11D00Display data input for (B).12D01O0h for black display13D02D00: LSB14D0315D04Driver IC carries out gamma conversion internally.16D0517D10Display data input for (G).18D11O0h for black display19D12D10: LSB20D1321D1422D1523D2024D2100h for black display25D2226D2327D2428D2529VSS30DE31STBYB33NC34NC35NC36NC37TEST138BLH38BLH30DE31Standby signal33NC0OPEN34NC35NC36NC37TEST238BLH34LED drive power source. (Anode side)	7	HSYNC	Horizontal sync signal input. (Negative polarity)
10VSSGround11D00Display data input for (B).12D01Oth for black display13D02D00: LSB14D0315D04Driver IC carries out gamma conversion internally.16D0517D10Display data input for (G).18D11Oth for black display19D12D10: LSB20D1321D14Driver IC carries out gamma conversion internally.22D1523D2024D2100h for black display25D2226D2327D2429VSS29VSS30DEInput data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST220Connect to Ground.38BLHLED drive power source. (Anode side)	8	VSYNC	Vertical sync signal input.(Negative polarity)
11D00Display data input for (B).12D01Oth for black display13D02D00: LSB14D0315D04Driver IC carries out gamma conversion internally.16D0517D10Display data input for (G).18D11Oth for black display19D12D10: LSB20D1321D1423D2024D2125D2226D2327D2428D2529VSS29VSS31STBYBStandby signal(Lo:Standby operation,Hi:Normal operation)32TEST133NC34NC35NC36NC38BLHLED drive power source. (Anode side)	9	CLK	Clock input for display. (Data Input on the falling edge)
12 D01 00h for black display 13 D02 D00:LSB D05:MSB 14 D03 Driver IC carries out gamma conversion internally. 16 D05 D00:LSB D01 17 D10 Display data input for (G). 00h for black display 18 D11 00h for black display D10:LSB D15:MSB 20 D13 D10:LSB D15:MSB D10:LSB D15:MSB 20 D13 D10:LSB D15:MSB D10:LSB D15:MSB 20 D13 D10:LSB D15:MSB D10:LSB D15:MSB 21 D14 Driver IC carries out gamma conversion internally. D11 D00 hor black display 22 D15 D20 Display data input for (R). D00 hor black display D20:LSB D20:LSB <t< td=""><td>10</td><td>VSS</td><td>Ground</td></t<>	10	VSS	Ground
13D02D00:LSBD05:MSB14D03Driver IC carries out gamma conversion internally.16D0517D10Display data input for (G).18D1100h for black display19D12D10:LSB20D1321D1422D1523D2024D2100h for black display25D22D24D10:LSB27D24D2529VSS29VSS30DEInput data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLHLED drive power source. (Anode side)	11	D00	Display data input for (B).
14D0315D04Driver IC carries out gamma conversion internally.16D0517D10Display data input for (G).18D11O0h for black display19D12D10:LSB20D1321D14Driver IC carries out gamma conversion internally.22D1523D2024D2100h for black display25D2226D2327D2429VSS29VSS30DE10put data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST223Connect to Ground.38BLHLED drive power source. (Anode side)	12	D01	00h for black display
15D04Driver IC carries out gamma conversion internally.16D0517D10Display data input for (G).18D1100h for black display19D12D10:LSB20D1321D14Driver IC carries out gamma conversion internally.22D1523D20Display data input for (R).24D2100h for black display25D22D2226D2327D24Driver IC carries out gamma conversion internally.28D2529VSS30DE10put data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLH44LED drive power source. (Anode side)	13	D02	D00:LSB D05:MSB
16D0517D10Display data input for (G).18D1100h for black display19D12D10:LSB20D1321D14Driver IC carries out gamma conversion internally.22D1523D20Display data input for (R).24D2100h for black display25D22D20:LSB26D2327D24Driver IC carries out gamma conversion internally.28D2529VSS29VSS31STBYBStandby signal(Lo:Standby operation,Hi:Normal operation)32TEST133NC34NC35NC36NC37TEST238BLHLED drive power source. (Anode side)	14	D03	
17D10Display data input for (G). 00h for black display18D1100h for black display19D12D10:LSB20D1321D1422D1523D2024D2125D2226D2327D2428D2529VSS29VSS30DEInput data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLH29LED drive power source. (Anode side)	15	D04	Driver IC carries out gamma conversion internally.
18D1100h for black display D10:LSBD15:MSB20D13D10:LSBD15:MSB21D14Driver IC carries out gamma conversion internally.22D15Display data input for (R).23D20Display data input for (R).24D2100h for black display25D22D20:LSB26D2327D2429VSS29VSS30DE101Input data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLH4LED drive power source. (Anode side)	16	D05	
19D1220D1321D1422D1523D2024D2100h for black display25D2226D2327D2429VSS29VSS30DE1011Input data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLH38BLH29LED drive power source. (Anode side)	17	D10	Display data input for (G).
20D1321D1422D1523D20Display data input for (R).24D2100h for black display25D22D20: LSB26D2327D24Driver IC carries out gamma conversion internally.28D2529VSSGround30DEInput data effective signal. (It is effective for the period of "H")31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	18	D11	00h for black display
21D14Driver IC carries out gamma conversion internally.22D15D1523D20Display data input for (R).24D2100h for black display25D22D20:LSB26D2327D24Driver IC carries out gamma conversion internally.28D2529VSS30DE10put data effective signal. (It is effective for the period of "H")31STBYB32TEST133NC34NC35NC36NC37TEST238BLHLED drive power source. (Anode side)	19	D12	D10:LSB D15:MSB
22D1523D20Display data input for (R).24D2100h for black display25D22D20:LSB26D2327D24Driver IC carries out gamma conversion internally.28D2529VSS30DEInput data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLHLED drive power source. (Anode side)	20	D13	
23D20Display data input for (R).24D2100h for black display25D22D20:LSB26D2327D2428D2529VSS30DEInput data effective signal. (It is effective for the period of "H")31STBYB33NC34NC35NC36NC37TEST238BLH38BLH2929292929292929303031323334343536373834343536373738343434343536373738343435363737383934343536373738393434353637373839393030303030303031 <td>21</td> <td>D14</td> <td>Driver IC carries out gamma conversion internally.</td>	21	D14	Driver IC carries out gamma conversion internally.
24D2100h for black display25D22D20:LSBD25:MSB26D23Driver IC carries out gamma conversion internally.28D2529VSSGround30DEInput data effective signal. (It is effective for the period of "H")31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	22	D15	
25D2226D2327D2428D2529VSS30DE1nput data effective signal. (It is effective for the period of "H")31STBYB32TEST1Connect to Ground.33NC34NC35NC36NC37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	23	D20	Display data input for (R).
26D2327D2428D2529VSS30DE1nput data effective signal. (It is effective for the period of "H")31STBYB32TEST1Connect to Ground.33NC34NC35NC36NC37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	24	D21	00h for black display
27D24Driver IC carries out gamma conversion internally.28D2529VSSGround30DEInput data effective signal. (It is effective for the period of "H")31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	25	D22	D20:LSB D25:MSB
28D2529VSSGround30DEInput data effective signal. (It is effective for the period of "H")31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	26	D23	
29VSSGround30DEInput data effective signal. (It is effective for the period of "H")31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	27	D24	Driver IC carries out gamma conversion internally.
30DEInput data effective signal. (It is effective for the period of "H")31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	28	D25	
31STBYBStandby signal (Lo:Standby operation,Hi:Normal operation)32TEST1Connect to Ground.33NCOPEN34NCOPEN35NCOPEN36NCOPEN37TEST2Connect to Ground.38BLHLED drive power source. (Anode side)	29	VSS	-
32 TEST1 Connect to Ground. 33 NC OPEN 34 NC OPEN 35 NC OPEN 36 NC OPEN 37 TEST2 Connect to Ground. 38 BLH LED drive power source. (Anode side)	30	DE	
33 NC OPEN 34 NC OPEN 35 NC OPEN 36 NC OPEN 37 TEST2 Connect to Ground. 38 BLH LED drive power source. (Anode side)	31	STBYB	Standby signal (Lo:Standby operation,Hi:Normal operation)
34 NC OPEN 35 NC OPEN 36 NC OPEN 37 TEST2 Connect to Ground. 38 BLH LED drive power source. (Anode side)	32	TEST1	Connect to Ground.
35 NC OPEN 36 NC OPEN 37 TEST2 Connect to Ground. 38 BLH LED drive power source. (Anode side)	33	NC	OPEN
36 NC OPEN 37 TEST2 Connect to Ground. 38 BLH LED drive power source. (Anode side)	34	NC	OPEN
37 TEST2 Connect to Ground. 38 BLH LED drive power source. (Anode side)	35	NC	OPEN
38 BLH LED drive power source. (Anode side)	36	NC	OPEN
	37	TEST2	
39 BLL LED drive power source. (Cathode side)	38	BLH	LED drive power source. (Anode side)
	39	BLL	LED drive power source. (Cathode side)

- Recommended connector: HIROSE ELECTRIC FH23 series [FH23-39S-0.3SHW(05)]

- Please make sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit.

Inconsistency in input signal assignment may cause a malfunction.

- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.



SPECIFICATIONS № 22TLM073

6. Absolute Maximum Rating

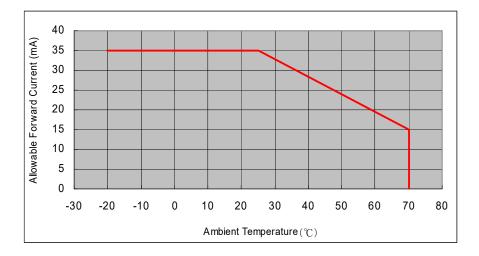
						VSS=0V
Item	Symbol	Condition	Ra	Rating		Applicable terminal
			MIN	MAX		
Supply voltage	VDD	Ta=25° C	-0.3	4.6	V	VDD
Logic interface voltage	VCCIO		-0.3	VDD	V	VCCIO
Input voltage for logic	VI		-0.3	VCCIO+0.3	V	CLK,VSYNC,HSYNC,DE D[05:00],D[15:10] D[25:20],STBYB,RESETB
Forward current	IL	Ta = 25° C		35	mA	BLH-BLL
		Ta = 70° C		15		
Storage temperature range	Tstg		-30	80	°C	
Storage humidity range	Hstg	Non condensing moisture at or lea				

7. Recommended Operating Conditions

	0 -						VSS=0V
Item	Symbol	Condition		Rating		Unit	Applicable terminal
			MIN	TYP	MAX		
Supply voltage	VDD		2.7	3.0	3.6	V	VDD
Logic interface voltage	VCCIO		1.7	1.8	VDD	V	VCCIO
Input voltage for logic	VI		0		VCCIO	V	CLK,VSYNC,HSYNC,DE D[05:00],D[15:10] D[25:20],STBYB,RESETB
Operational temperature range	Тор	Note1,2	-20	+25	+70	°C	Panel surface temperature
Operating humidity range	Нор	Ta ≦ 40° C	20		85	%	
		Ta > 40° C	Non condensing in an environmental moisture at or less than 40°C85%RH.				

Note1: This monitor is operatable in this temperature range. With regard to optical characteristics, refer to Item 10."Characteristics".

Note 2: Acceptable Forward Current to LED is up to 15mA, when Ta=+70 °C. Do not exceed Allowable Forward Current shown on the chart below.



8. Electrical Characteristics

8.1 DC Characteristics

8.1.1 Display Module

(Unless otherwise noted, Ta=25°C,VDD=3.0V,VCCIO=1.8V,VSS=0V)								
Item	Symbol	Condition		Rating		Unit	Applicable terminal	
			MIN	TYP	MAX			
Input Signal Voltage	VIH	VCCIO=1.7-VDD	0.7×VCCIO		VCCIO	V	CLK,VSYNC,HSYNC, DE,D[05:00],	
	VIL		0		0.3×VCCIO	V	D[15:10],D[25:20], STBYB,RESETB	
Operating	IDD	fCLK=19.8MHz		(8.0)	(16.0)	mA	VDD	
Current	ICCIO	Color bar display		(0.6)	(1.2)	mA	VCCIO	
Stand-by	IDDS	Other input with		5.0	15.0	μA	VDD	
Current	ICCIOS	constant voltage			1.0	μA	VCCIO	

8.1.2 Backlight

ltem	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL25	Ta=25 ℃	—	(10.0)	35.0	mA	BLH — BLL
	IL70	Ta=70 ℃	—	-	15.0	mA	
Forward voltage	VL	Ta=25 ℃	—	(16.3)	(17.2)	V	
(Reference		IL=10.0mA					
Value)							
Estimated Life	LL	Ta=25 ℃	_	50,000	_	hrs	
of LED		IL=10.0mA					
		Note					

Note: - The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.

- This figure is given as a reference purpose only, and not as a guarantee.

- This figure is estimated for an LED operating alone.

As the performance of an LED may differ when assembled as a monitor.

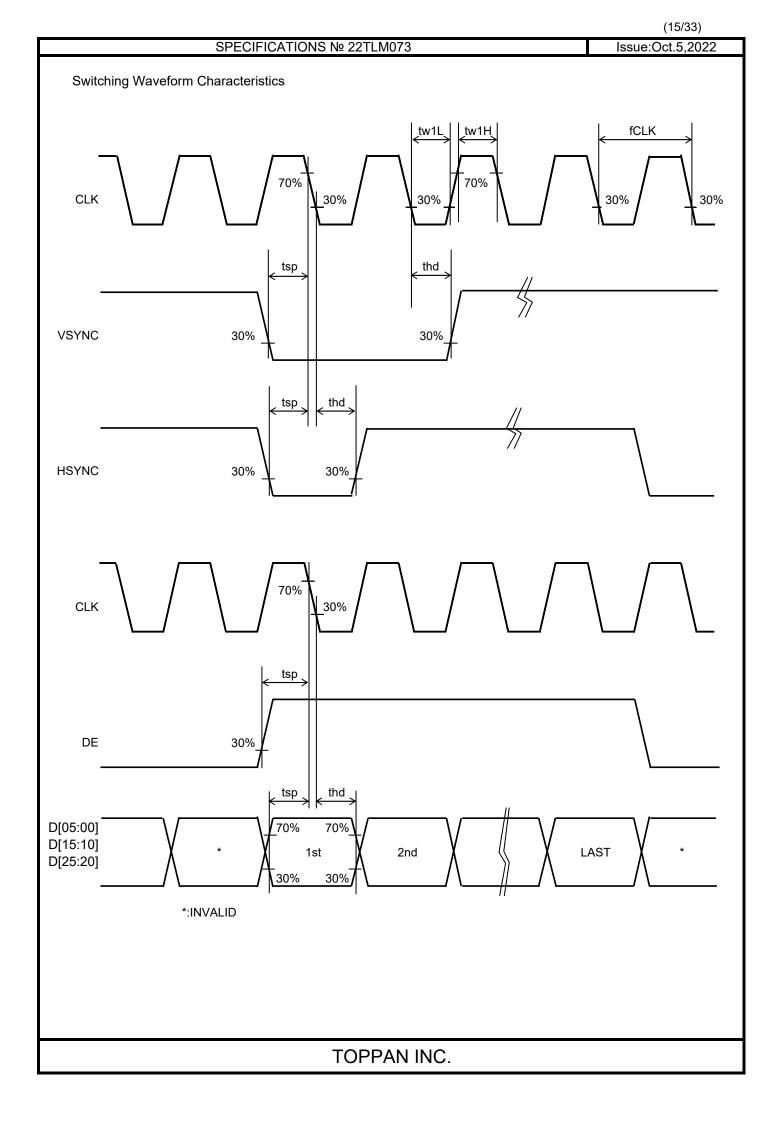
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

SPECIFICATIONS № 22TLM073

8.2 AC Characteristics

(Unless otherwise noted, Ta=25°C,VDD=3.0V,VCCIO=1.8V,VSS=0V)

Item	Symbol	Condition		Rating			Applicable terminal
			MIN	TYP	MAX		
CLK frequency	fCLK		18	19.8	27	MHz	CLK
CLK Low period	tw1L	0.3×VCCIO or less	10			ns	
CLK High period	tw1H	0.7×VCCIO or more	10			ns	
Setup time	tsp		10			ns	CLK,VSYNC,
							HSYNC,DE,
Hold time	thd		10			ns	D[05:00],D[15:10]
							D[25:20]

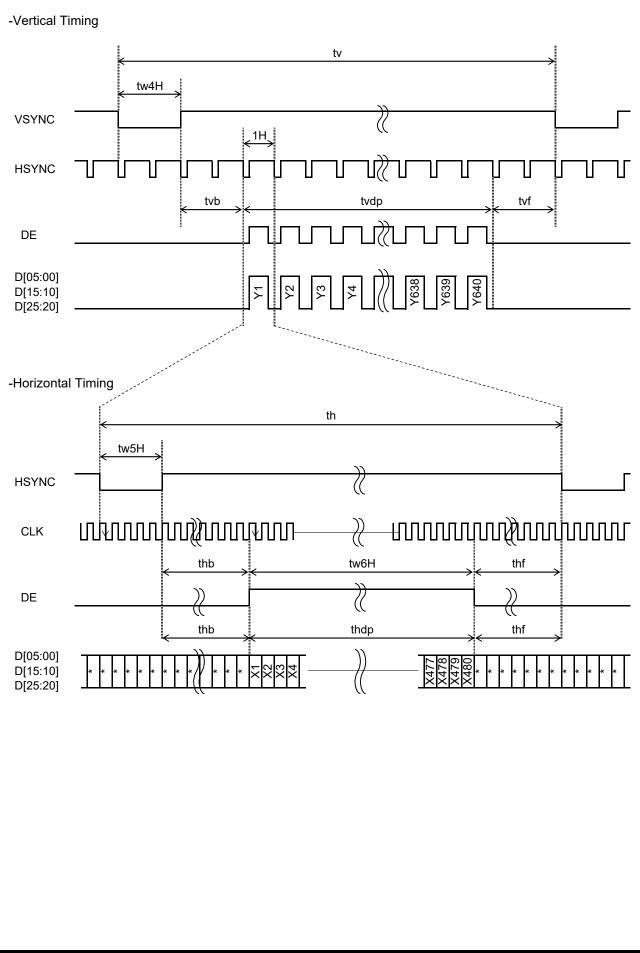


8.3 Input Timing Characteristics

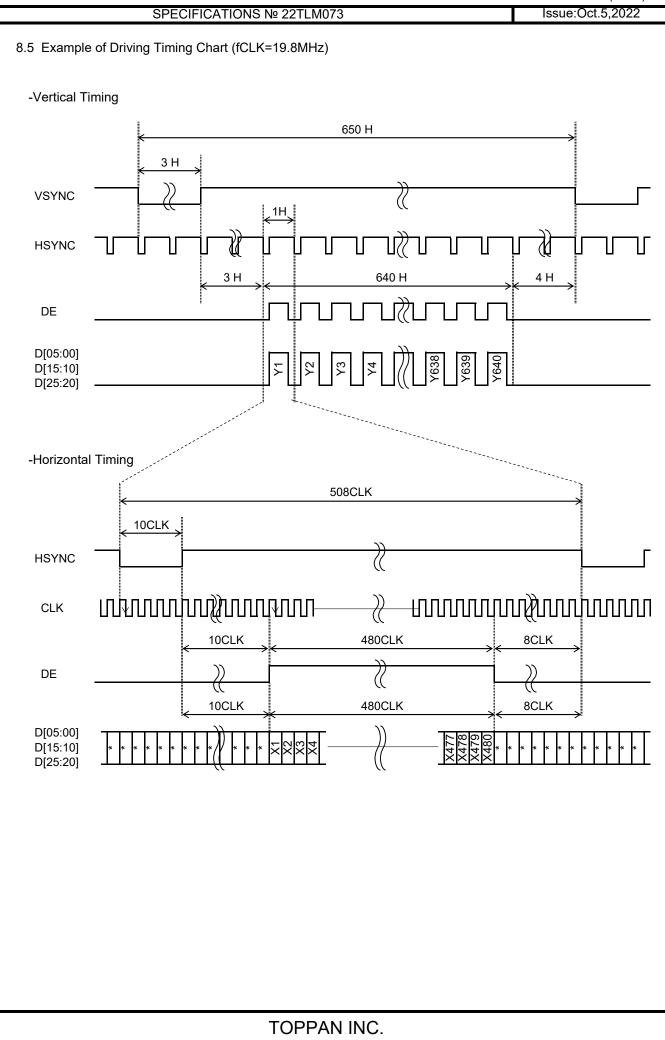
Item	Symbol		Rating		Unit	Applicable terminal	
		MIN	TYP	MAX			
CLK Frequency	fCLK	18	19.8	27	MHz	CLK	
VSYNC Frequency Note	fVSYNC	54	60	66	Hz	VSYNC	
VSYNC Cycle	tv	646	650	700	Н	VSYNC,HSYNC	
VSYNC Pulse Width	tw4H	2	3	50	Н		
Vertical Back Porch	tvb	2	3	50	Н	VSYNC,HSYNC,DE,	
Vertical Front Porch	tvf	2	4	50	Н	D[05:00],D[15:10],D[25:20]	
Vertical Display Period	tvdp		640		Н		
HSYNC frequency	fHSYNC		39.0	50.0	kHz	HSYNC	
HSYNC Cycle	th	504	508	630	CLK	CLK,HSYNC	
HSYNC Pulse Width	tw5H	5	10	140	CLK		
Horizontal Back Porch	thb	5	10	140	CLK	CLK,HSYNC,DE,	
Horizontal Front Porch	thf	5	8	140	CLK	D[05:00],D[15:10],D[25:20]	
Horizontal data start Point	tw5H+thb	19		145	CLK	1	
Horizontal Blanking Period	tw5H+thb+thf	24		150	CLK	1	
DE Pulse Width	tw6H		480		CLK	CLK,DE	
Horizontal Display Period	thdp		480		CLK	CLK,DE,	
						D[05:00],D[15:10],D[25:20]	

Note: This is recommended spec to get high quality picture on display. It is customer's risk to use out of this frequency.









	(19/33)
SPECIFICATIONS № 22TLM073	Issue:Oct.5,2022
9 Power ON/OFF sequence	
VDD Min 0ms *1	
VCCIO _/ Min 1ms *2	
RESETB	
STBYB Min 0ms *3	i4 │
VSYNC	
	www
	www
	www
DISP ON/	
Max 300ms	1
Min 0ms → <u> </u>	
*1 Please start up VDD and VCCIO at the same time or in order of VDD>	VCCIO.
*2 After the power supply,Please execute RESETB.	
*3 There is no regulations at time until each signal is supplied from RESETB"H" But meanwhile, It is necessary to fix each signal to "H"or"L".	
*4 It is necessary to supply VSYNC and CLK for 15 frames or more from STBYB "L turning off the power supply without leaving the afterimage.	" to
TOPPAN INC.	

10. Characteristics

10.1 Optical Characteristics

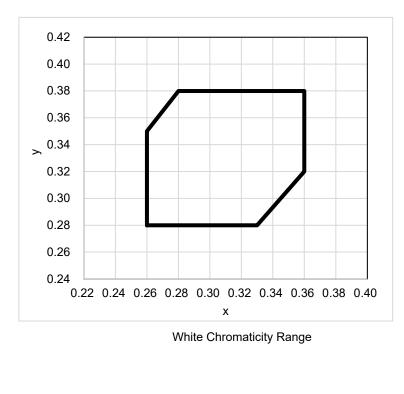
(Measurement Condition)

Measuring instruments : CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS), EZcontrastXL88 (ELDIM) Driving condition : VDD=3.0V,VCCIO=1.8V, VSS=0V, Optimized VCOMDC

Backlight: IL= (10.0) mA Measured temperature: Ta = 25°C

	Item	Symbol	Condition	MIN	TYP	MAX	Unit	Note №	Remark
Response time	Rise time + Fall time	TON + TOFF	[Data]= 00h← → 3Fh	-	-	(100)	ms	1	
Contrast ratio	Backlight ON	CR	[Data]= 3Fh / 00h	(480)	(800)	-		2	
Con	Backlight OFF			-	(2.6)	-			
g	Left	θL	[Data]=	(80)	-	-	deg	3	
Viewing angle	Right	θR	3Fh / 00h	(80)	-	-	deg		
/iev an	Up	φU	$CR \ge (10)$	(80)	-	-	deg		
/	Down	φD		(80)	-	-	deg		
White	Chromaticity	х	[Data]= 3Fh	White ch	romaticit	y range		4	
		у							
Cente	er Brightness		[Data]= 3Fh	(230)	(330)	-	cd/ m ²	5	
Bright	tness distribution		[Data]= 3Fh	(70)	-	-	%	6	
Burn-	in			No noticeable burn-in image shall be observed after (2) hours of window pattern display.				7	

* Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics and Performance".



′\M/hit≏	Chromaticity	(Range)
vvnite	Chiomaticity	range)

Х	У
(0.26)	(0.28)
(0.33)	(0.28)
(0.36)	(0.32)
(0.36)	(0.38)
(0.28)	(0.38)
(0.26)	(0.35)

(21/33)

SPECIFICATIONS № 22TLM073

10.2 Temperature Characteristics

(Measurement Condition)

Measuring instruments : CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS) Driving condition : VDD=3.0V,VCCIO=1.8V, VSS=0V, Optimized VCOMDC Backlight : IL= (10.0) mA

Item		Symbol	Specif	Remark	
			Ta = (-20) °C	Ta = (70) °C	1
Response time	Rise time	TON	(500) msec or less	(80) msec or less	
	+	+			
	Fall time	TOFF			
Contrast ratio		CR	(200) or more	(200) or more	Backlight ON
Display Quality			No noticeable display defect or ununiformity		
			should be observed.		

30cm

90

11. Criteria of Judgment

11.1 Defective Display and Screen Quality

Test Condition: Observed TFT-LCD monitor from front during operation with the following conditions Driving Signal: Raster Patter (RGB, white, black) Signal condition: [Data]:00h, (TBD)h, 3Fh (3steps) Observation distance: 30 cm Illuminance: 200 to 350 lx

Backlight: IL=(10.0)mA

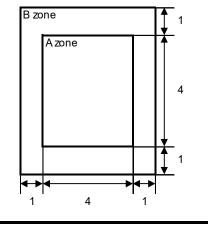
De	efect item	Defect content		Criteria
	Line	Black, white or color li	ne, 3 or more neighboring defective dots	Not exists
	defect			
lit∨	Dot	Uneven brightness on	dot-by-dot base due to defective	Refer to table 1
Quality	defect	TFT or CF, or dust is o	counted as dot defect	
		(brighter dot, darker d	ot)	
Display		High bright dot: Visible	e through 2% ND filter at [Data]=00h	
Di Si		Low bright dot: Visible	e through 5% ND filter at [Data]=00h	
		Dark dot: Appear dark	through white display at [Data]=(TBD)h	
		Invisible through 5% N	ID filter at [Data]=00h	Acceptable
	Stain	Uneven brightness (w	hite stain, black stain etc)	Invisible through 5% ND filter at Black screen.
				Invisible through 1% ND filter at other screen.
lit∨	Foreign	Point-like	0.25mm< φ	N=0
βua	Foreign particle		0.20mm< φ ≦0.25mm	N≦2
			φ ≦0.20mm	Acceptable
Screen		Liner	3.0mm < length and 0.08mm < width	N=0
ŝ			length \leq 3.0mm or width \leq 0.08mm	Acceptable
	Others			Use boundary sample
				for judgment when necessary

 $\phi(mm)$: Average diameter = (major axis + minor axis)/2 Permissible number: N

Table1

	High	Low	Dark		
Area	bright	bright	dot	Total	Criteria
	dot	dot			
А	0	2	2	3	Permissible distance between same color bright dots
					(includes neighboring dots): 3 mm or more
В	2	4	4	6	Permissible distance between same color high bright dots
					(includes neighboring dots): 5 mm or more
Total	2	4	4	7	

<Portrait model>



Division of A and B areas B area: Active area Dimensional ratio between A and B areas: 1: 4: 1 (Refer to the left figure)

SPECIFICATIONS № 22TLM07

11.2 Screen and Other Appearance

Testing conditions $\label{eq:observation} \begin{array}{l} \mbox{Observation distance: } 30\mbox{ cm} \\ \mbox{Illuminance: } 1200 \sim 2000\mbox{ lx} \end{array}$

	Item	Criteria	Remark
	Flaw	Ignore invisible defect when the backlight is on.	Applicable area: Active area only
L.	Stain		(Refer to the section 3.2 Outward Form)
rize	Dirt		
ola	Bubble		
٩	Stain Dirt Bubble Dust		
	Dent		
S	case	No functional defect occurs	
FF	PC Oc	No functional defect occurs	

SPECIFICATIONS № 22TLM073

12. Reliability Test

	Test item	Test condition	number of failures / number of examinations			
	High temperature storage	Ta = 80°C 240hrs	TBD			
	Low temperature storage	Ta = -30°C 240hrs	TBD			
	High temperature &	Ta = 60° C, RH = 90° , 240hrs	TBD			
L 1	high humidity storage	non condensing X4011S	סטו			
Durability test	High temperature operation	Tp = 70°C 240hrs	TBD			
ity	Low temperature operation	Tp = -20°C 240hrs	TBD			
abil						
Dura	High temperature &		TBD			
	high humidity operation	non condensing X	TDD			
	Thermal shock storage	-30°C ↔ 80°C (30min / 30min) 100cycles	TBD			
	Lightfastness	Xenon Blackpanel 63±3°C non-shower	TBD			
-		450W/m²(300~700nm) non-operating Integral dose 800MJ/m²				
	Electrostatic discharge test	Confirms to EIAJ ED-4701/300, C=200pF,R=0Ω,V=±200V	TBD			
est	(Non operation)	Each 3 times of discharge on and power supply				
alt		and other terminals.				
ent	Surface discharge test	C=250pF, R=100Ω, V=±(TBD)kV	TBD			
ШШ	(Non operation)	Each 5 times of discharge in both polarities				
/iro		on the center of screen with the case grounded.				
en	Vibration test	Total amplitude 1.5mm, f=10 \sim 55Hz,	TBD			
a		X,Y,Z directions for each 2 hours				
Mechanical environmental test	Impact test	Use TOPPAN original jig (see next page) and	TBD			
ç		make an impact with peak acceleration of 1000m/s ² for 6 msec				
Re		with half sine-curve at 3 times to each X, Y, Z directions				
		in conformance with JIS C 60068-2-27-2011.				
_	Packing vibration-proof test					
Packing test	.	X,Y, Zdirection for each 30 minutes.				
ackin test	Packing drop test	Drop from 75cm high.	TBD			
م	5 1	1 time to each 6 surfaces, 3 edges, 1 corner				
		·				

Note:Ta=ambient temperature Tp=Panel temperature

% The profile of high temperature/humidity storage and High Temperature/humidity operation (Pure water of over $10M\Omega \cdot cm$ shall be used.)

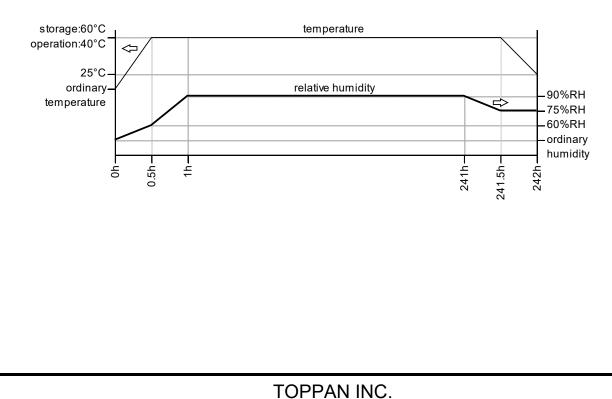
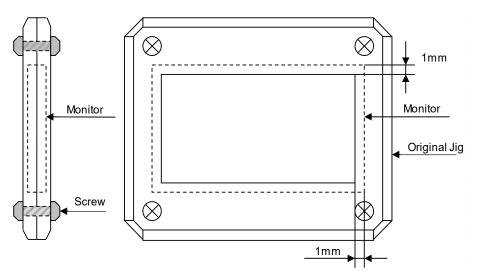


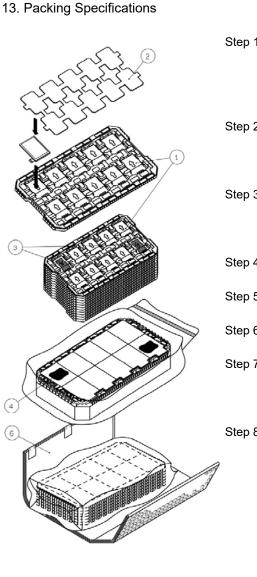
Table2. Reliability Criteria

The parameters should be measured after leaving the monitor at the ordinary temperature for 24 hours

Item	Standard	Remark
Display quality	No visible abnormality shall be seen.	
	(Except for unevenness by Pol deterioration.)	
Contrast ratio	200 or more	Backlight ON

TOPPAN Original Jig



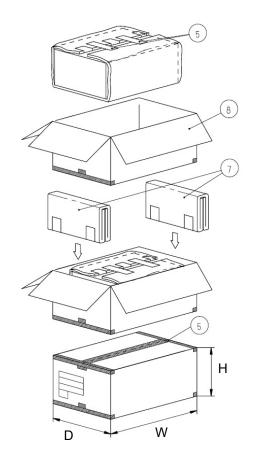


- Step 1. Each product is to be placed in one of the cut-outs of the tray with the display surface facing upward.Foam sheet A are to be placed on the products in the tray. (10 products per tray)
- Step 2. Each tray is to be piled up in same orientation and the trays be in a stack of 10.One empty tray is to be put on the top of stack of 10 trays.
- Step 3. 2 packs of moisture absorbers are to be placed on the top tray as shown in the drawing.Put piled trays into a sealing bag.
- Step 4. Vacuum and seal the sealing bag with the vacuum sealing machine.
- Step 5. The stack of trays in the plastic back is to be wrapped with B SHEET A.
- Step 6. The wrapped trays are placed in the carton.
- Step 7. B SHEET B are to be inserted into a outer carton with same orientation. The outer carton is to be sealed in H-shape with packing tape as shown in the drawing.
- Step 8. The model number, quantity of products, and shipping date are to be printed on the outer carton.If necessary, shipping labels or impression markings are to be put on the outer carton.

Remark: The return of packing materials is not required.

Packing item name		Specs., Material	
① Tray A-PET		A-PET	
2	FOAM SHEET	Anti-static polyethylene	
3	Drier	Moisture absorber	
4	Sealing bag		
5	Packing tape		
6	B SHEET A	Anti-static air bubble sheet	
\bigcirc	B SHEET B	Anti-static air bubble sheet	
8	Outer carton	Corrugated cardboard	

Dimension of outer carton		
D : Approx.	(356mm)	
W : Approx.	(664mm)	
H : Approx.	(182mm)	
Quantity of products packed i	n one carton: 100	
Gross weight : Approx.	6.1 kg	



14. Handling Instruction

14.1 Cautions for Handling LCD panels

(1) [
	Do not make an impact on the LCD panel glass because it may break and you may get injured from it.
. ,	f the glass breaks, do not touch it with bare hands. (Fragment of broken glass may stick you or you cut yourself on it.
(3) It	f you get injured, receive adequate first aid and consult a medial doctor.
(Do not let liquid crystal get into your mouth. (If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.)
(f liquid crystal adheres, rinse it out thoroughly. (If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.
	f you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.
(7) E	Do not connect or disconnect this product while its application products is powered on.
(8) E	Do not attempt to disassemble or modify this product as it is precision component.
v F	f a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please. Please insulate it with the insulating tape etc. if necessary. The defective operation is caused, and there is a possibility to generation of heat and the ignition.
L	Since excess current protection circuit is not built in this TFT module, there is the possibility that _CD module or peripheral circuit become feverish and burned in case abnormal operation is generated. We recommend you to add excess current protection circuit to power supply.
b V c E	The devices on the FPC are damageable to electrostatic discharge, because the terminals of the devices are exposed. Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors. Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.



This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.

14.2 Precautions for Handling

- Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
 Do not touch the surface of the monitor as it is easily scratched.
- Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge.
 Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.
- 3) Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.
- 4) Do not use or storage the TFT monitors at high temperature and high humidity environment. Particularly, never use or storage the TFT monitors at a location where condensation builds up.
- 5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.
- Do not stain or damage the contacts of the FPC cable .
 FPC cable needs to be inserted until it can reach to the end of connector slot.
 During insertion, make sure to keep the cable in a horizontal position to avoid an oblique insertion.
 Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.
- 7) The FPC cable is a design very weak to the bend and the pull as it is fixed with the tape. Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable.
- 8) Peel off the protective film on the TFT monitors during mounting process.
 Refer to the section 14.5 on how to peel off the protective film.
 We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.

14.3 Precautions for Operation

- Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.
- In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.
- Do not plug in or out the FPC cable while power supply is switch on. Plug the FPC cable in and out while power supply is switched off.
- 4) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.
- 5) Do not display a fixed image on the screen for a long time.
 Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time.
 Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

14.4 Storage Condition for Shipping Cartons

(Storage environment)

Temperature	0 to 40° C
Humidity	60%RH or less
	No-condensing occurs under low temperature with high humidity condition.
Atmosphere	No poisonous gas that can erode electronic components and/or
	wiring materials should be detected.
 Time period 	1 year
 Unpacking 	To prevent damages caused by static electricity, anti-static precautionary measures
	(e.g. earthing, anti-static mat) should be implemented.
	After unpack, keep product in the appropriate condition,
	otherwise bubble seal of Protective film may be printed on Polarizer.
Maximum piling up	8 cartons(excluding the bottom)

*Conditions to storage after unpacking

(Storage environment)

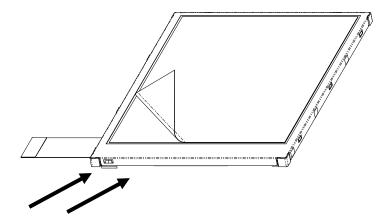
Temperature	0 to 40° C
Humidity	60%RH or less
	No-condensing occurs under low temperature with high humidity condition.
Atmosphere	No poisonous gas that can erode electronic components and/or
	wiring materials should be detected.
 Time period 	1 year (Shelf life)
Others	Keep/ store away from direct sunlight
	Storage goods on original tray made by TOPPAN.

14.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

A) Work Environment

- a) Humidity: 50 to 70 %RH, Temperature15 to 27 °C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps. Use an electrostatic neutralization blower.
- c) Anti-static treatment should be implemented to work area's floor.
 Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.
- B) Work Method
 - The following procedures should taken to prevent the driver ICs from charging and discharging.
 - a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower left when FPC case is placed at the bottom.
 Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
 - b) Put an adhesive tape (Scotch tape, etc) at the lower left corner area of the protective film to prevent scratch on surface of TFT monitors.
 - c) Peel off the adhesive tape slowly (spending more than 2 secs to complete) by pulling it to opposite direction.



Blower wind direction (Set an ion blower with its adequate conditions.)

14.6 Warranty

TOPPAN is only liable to defective goods which is stored and used under the condition complying with this specifications and returned within 1 (one) year. Warranty caused by manufacturing defect shall be conducted by replacement of goods or refundment at unit price.

APPENDIX

Reference Method for Measuring Optical Characteristics and Performance

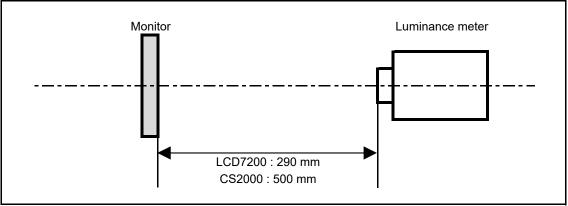
1. Measurement Condition (Backlight ON)

Measuring instruments: CS2000 (KONICA MINOLTA), LCD7200 (OTSUKA ELECTRONICS), EZcontrastXL88 (ELDIM) Driving condition: Refer to the section "Optical Characteristics"

Measured temperature: 25°C unless specified

Measurement system: See the chart below. The luminance meter is placed on the normal line of measurement system. Measurement point: At the center of the screen unless otherwise specified

Dark box at constant temperature

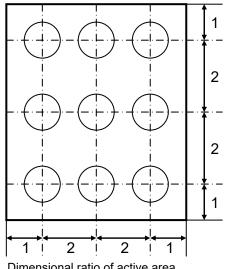


*Measurement is made after 30 minutes of lighting of the backlight.

Measurement point:

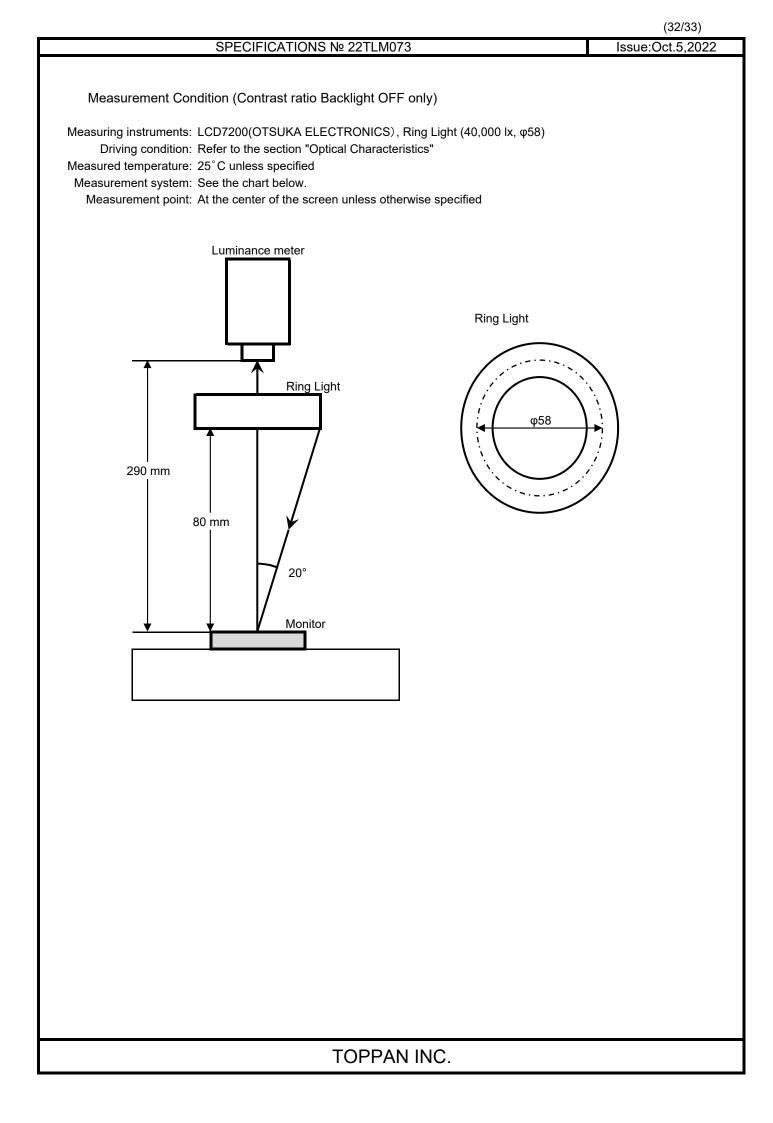
At the center point of the screen Brightness distribution: 9 points shown in the following drawing.

<Portrait model>



Dimensional ratio of active area

Backlight IL=(10.0)mA



SPECIFICATIONS № 22TLM073

Notice	Item	Test method	Measuring instrument	Remark
1	Response time	Measure output signal waveform by the luminance meter when raster of window pattern is changed from white to black and from black to white. Black 100% 90% 10% 10% 10% TON TOFF	LCD7200	Black display [Data]=00h White display [Data]=3Fh TON Rise time TOFF Fall time
2	Contrast ratio	Measure maximum luminance Y1([Data]=3Fh) and minimum luminance Y2([Data]=00h) at the center of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 7.8mmφ(CS2000) Diameter of measuring point: 3mmφ(LCD7200)	CS2000 LCD7200	Backlight ON Backlight OFF
3	Viewing angle Horizontalθ Verticalφ	Move the luminance meter from right to left and up and down and determine the angles where contrast ratio is (10).	EZcontrastXL88	
4	White chromaticity	Measure chromaticity coordinates x and y of CIE1931 colorimetric system at [Data] = 3Fh Color matching function: 2°view measurement angle: 1°	CS2000	
5	Center brightness	Measure the brightness at the center of the screen.	CS2000	
6	Brightness distribution	(Brightness distribution) = 100 x B/A % A : max. brightness of the 9 points B : min. brightness of the 9 points	CS2000	
7	Burn-in	Visually check burn-in image on the screen after 2 hours of "window display" ([Data]=00h/3Fh).		At optimized VCOMDC



Our company network supports you worldwide with offices in Germany, Austria, Switzerland, the UK and the USA. For more information please contact:

Headquarters

Fortec Group Members





DISTEC

A FORTEC GROUP MEMBER

FORTEC Elektronik AG Augsburger Str. 2b 82110 Germering

Phone: E-Mail: Internet: +49 89 894450-0 info@fortecag.de www.fortecag.de

Distec GmbH Office Vienna Nuschinggasse 12 1230 Wien

Phone: E-Mail: Internet: +43 1 8673492-0 info@distec.de www.distec.de

Distec GmbH Augsburger Str. 2b 82110 Germering

Phone: E-Mail:

Internet:

+49 89 894363-0 info@distec.de www.distec.de

ALTRAC AG

Bahnhofstraße 3 5436 Würenlos

Phone: E-Mail: Internet: +41 44 7446111 info@altrac.ch www.altrac.ch

Display Technology Ltd.

Osprey House, 1 Osprey Court Hichingbrooke Business Park Huntingdon, Cambridgeshire, PE29 6FN

Phone: E-Mail: Internet: +44 1480 411600 info@displaytechnology.co.uk www. displaytechnology.co.uk

Apollo Display Technologies, Corp. 87 Raynor Avenue, Unit 1Ronkonkoma, NY 11779

Phone: E-Mail: Internet: +1 631 5804360 info@apollodisplays.com www.apollodisplays.com



Austria













